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## APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

FOURTH SEMESTER B.TECH DEGREE EXAMINATION(S), DECEMBER 2019

**Course Code: EC206** 

**Course Name: COMPUTER ORGANISATION** 

Max. Marks: 100 Duration: 3 Hours

## **PART A** Marks Answer any two full questions, each carries 15 marks a) Explain with neat diagram a 32 bit ripple carry adder. 1 (3) b) With neat diagram explain Arithmetic Logic Unit (5) c) Explain the R- type instruction format of MIPS with example (3) Translate the following machine language code into MIPS assembly language: (4) 0xAD310004 2 a) Design a 4×4 binary multiplier. Illustrate with an example (7) b) Write notes on MIPS register set. (4) Translate the following MIPS assembly code to MIPS machine language code in (4) hexadecimal form: lw \$t2, 32 (\$0) a) Explain how floating point numbers are represented in computer's memory. (6) b) Differentiate Big-Endian and Little-Endian machines (4) c) Explain load word and store word instructions with examples (5) PART B Answer any two full questions, each carries 15 marks a) With examples, explain the different addressing modes available in MIPS. (10)b) Explain the control unit of a multi cycle processor (5) a) Explain the various steps for executing a program (9)b) What are the weaknesses of a single cycle processor. How are they eliminated in a (6) multi cycle processor? a) What are exceptions? How the exceptions are handled? (7) b) Draw and explain datapath for single cycle implementation for R-type (8) instructions. PART C Answer any two full questions, each carries 20 marks a) With the help of a diagram, explain the concept of memory hierarchy. 7 (5) b) Distinguish between Programmed I/O and Interrupt driven I/O (5) Explain how a virtual address is translated into a physical address in virtual (10)memory using page table. a) Differentiate between SRAM and DRAM (6)

c) Explain the concept of cache memory. Also define Miss Rate, Hit Rate and

(10)

Average memory access time.