APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

Scheme for Valuation/Answer Key

Scheme of evaluation (marks in brackets) and answers of problems/key

SEVENTH SEMESTER B.TECH DEGREE EXAMINATION, DECEMBER 2018

Course Code: AE461

Course Name: ARM SYSTEM ARCHITECTURE

Max. Marks: 100			Ouration: 3 Hours	
		PART A		
		Answer any two full questions, each carries 15 marks.	Marks	
1	a)	Explanation(7 marks)	(7)	
	b)	CPSR register (4 marks), Explanation(4marks)	(8)	
2	a)	Explanation (7 marks)	(7)	
	b)	Types of exception (3 marks), explanation (5 marks)	(8)	
3	a)	Functional requirements(4marks),non-functional requirements (4 marks)	(8)	
	b)	Need for pipelining(3 marks), ARM 7 pipelining(4 marks)	(7)	
		PART B		
		Answer any two full questions, each carries 15 marks.		
4	a)	Explanation of each instruction(2 marks each)	(8)	
	b)	Registers (3 marks), Explanation (4 marks)	(7)	
5	a)	Block diagram (3 marks), Explanation(5 marks)	(8)	
	b)	Block diagram (3 marks), Explanation (4 marks)	(7)	
6	a)	Define (3 marks), explanation (4 marks)	(7)	
	b)	Configuration steps(8 marks)	(8)	
		PART C		
7	(۵	Answer any two full questions, each carries 20 marks. Explanation user(3), kernel(3), supervisor(4)	(10)	
/	a)		(10)	
	b)	Explanation(5 marks)	(5)	
	c)	Explain (5 marks)	(5)	
8	a)	Data types(5 marks), Alignments(5 marks)	(10)	
	b)	Explanation (4marks)	(4)	
	c)	Program with comments(4 marks), Explanation(2 marks)	(6)	
9	a)	Diagram(5marks),Explanation(5 marks)	(10)	
	b)	Configuration steps (10 marks)	(10)	
